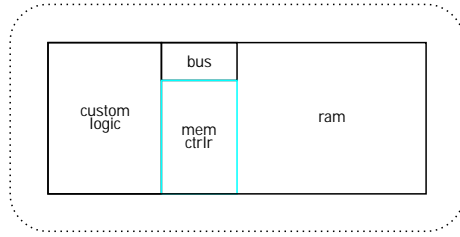
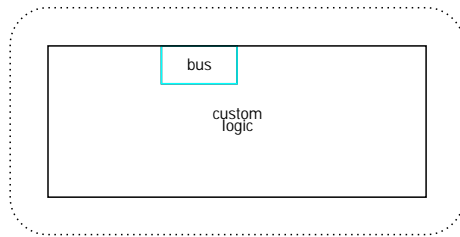


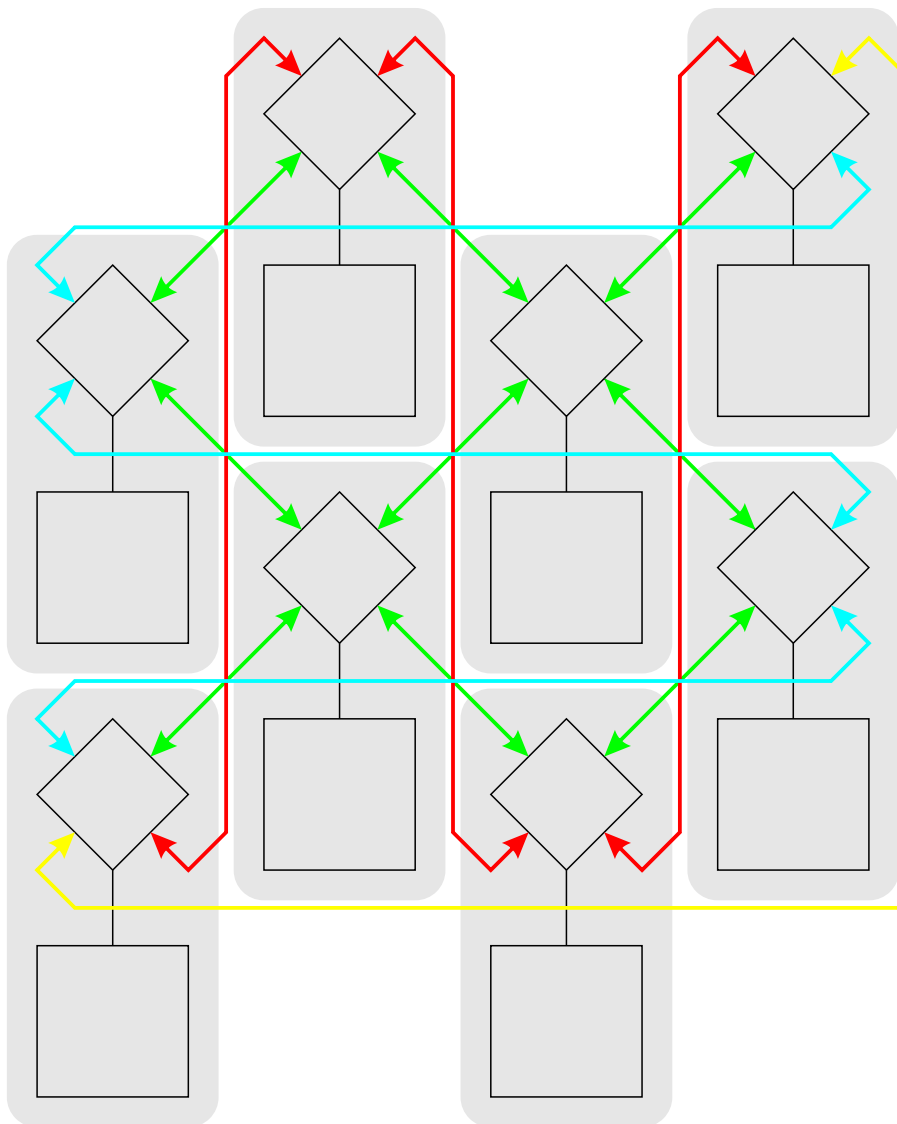
P-type



S-type



C-type



- standard connection
- wraparound connection
- wraparound connection
- wraparound connection

wraparound connections are optional, but recommended

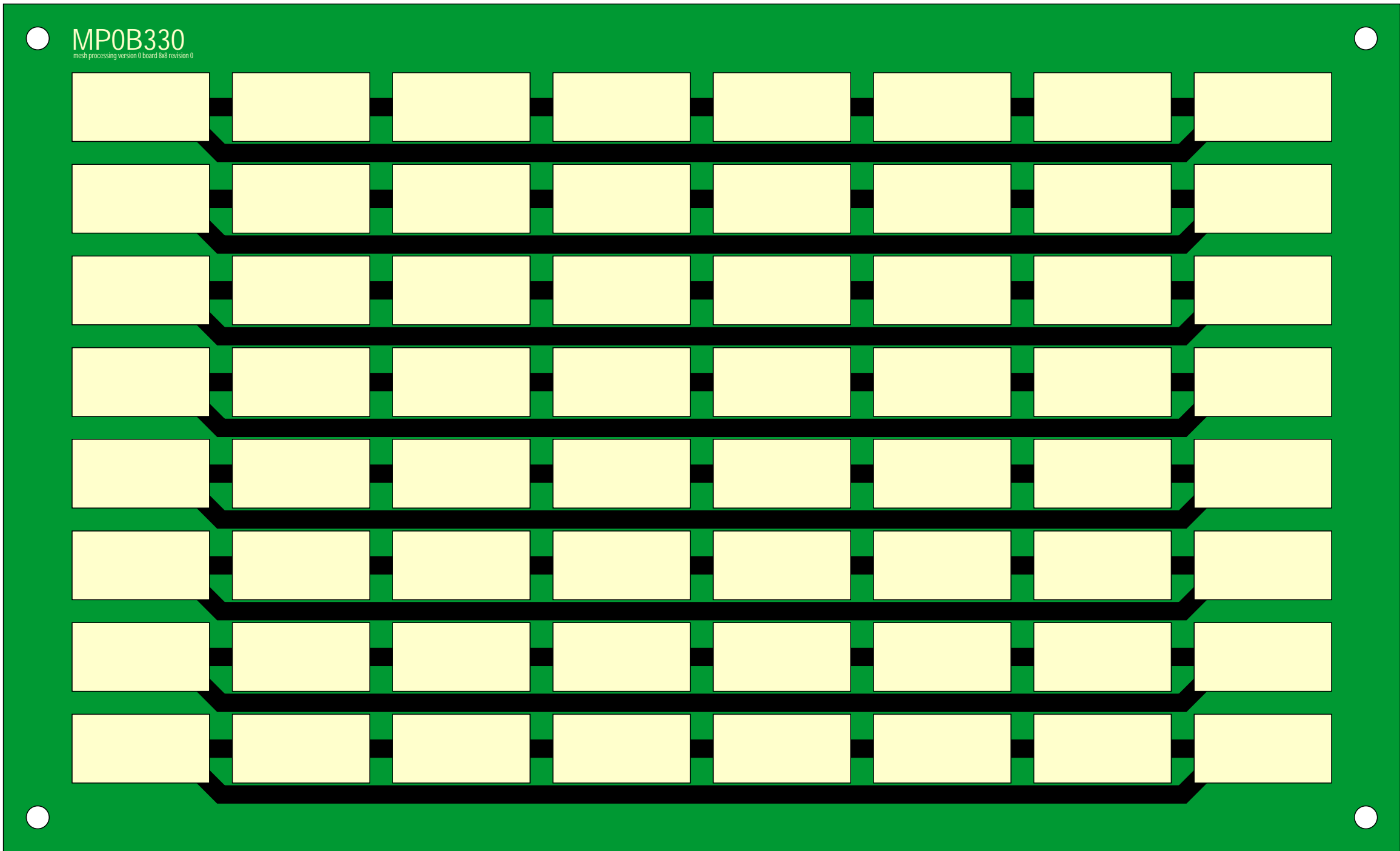
the memory controller inside every node acts also as a ccNUMA controller

the bus controller inside each node acts as a wire-speed bus router

using wraparound connections the maximum distance between each pair of nodes is $\log_2(N)$, where N is the number of nodes: this effectively creates a pyramidal NUMA architecture without bus bottlenecks

MP0B330

mesh processing version 0 board 848 revision 0



MP0B000
MP0B100
MP0B110
MP0B210
MP0B220
MP0B330
MP0B440

common power rails:

+12V
-12V
+5V
-5V
+3.3V
-3.3V
GNDD
GNDA
P0
P1
P2
P3
P4
P5
P6
P7

common data channels:

I2C
JTAG

common connections:
not displayed

MP0B330

mesh processing version 0 board 848 revision 0

